

DATASHEET:

DURALINK DATA PIPELINE WITH CUSTOM CONTROLLER CHIP

Uncompressed, dot-level control for print data streams, from the RIP to the printhead, for each color



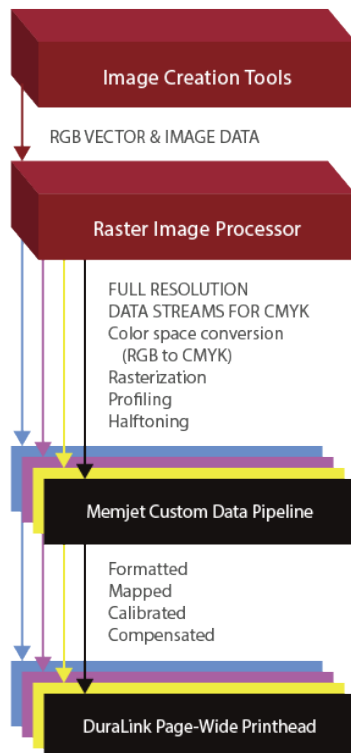
MEMJET HIGH-SPEED DATA PIPELINE

The DuraLink print data pipeline provides high-speed, uncompressed, dot-level control of print data streams, from the RIP to the printheads for each color.

The custom FPGA is designed for high performance, single-color printheads. This chip supports dot-level control of the print data for extraordinary printing speeds (up to 203.6 meters per minute (668.1 fpm), from raster image processor (RIP) to the printhead.

DATA PIPELINE AND CONTROLLER CHIP MAIN FEATURES

- Dot-level control of print data streams
- No compression
- Full resolution data streams to the DuraLink printhead
- Independent direct control of color management



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DURALINK
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DURALINK PIPELINE AND CONTROLLER CHIP

Resolution	Meters per Minute	Feet per Minute	Inches per Second
1600 x 1585	74.5 m/in	244.5 fpm	48.9 ips
1600 x 1260	93.7 m/in	307.5 fpm	61.5 ips
1600 x 790	149.5 m/in	490.5 fpm	98.1 ips
1600 x 580	203.6 m/in	668.1 fpm	133.6 ips

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